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CONFIRMATION NO. 5195

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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This appln claims benefit of 60/212,000 06/16/2000  
*yes sbj*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
*None sbj*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 06/11/2001

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>sbj</i> Examiner's Signature _____ Initials _____	STATE OR COUNTRY NY	SHEETS DRAWING 12	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 2
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TITLE  
 Latch structure for interlocked pipelined CMOS (IPCMOS) circuits

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